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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ko, *et al.* Docket No.: TSM03-0615
Serial No.: 10/729,095 Art Unit: 2811
Filed: December 5, 2003 Examiner: Hung K. Vu
For: Structure and Method of a Strained Channel Transistor and a Second Semiconductor Component in an Integrated Circuit

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Date of Deposit: October 14, 2005

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Form PTO/SB/08B with 2 References Cited (1 page)
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Respectfully submitted,

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Commissioner for Patents
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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08B that may be considered material to the examination of the above-identified application.

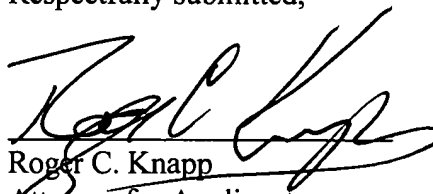
This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first. Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm. A duplicate copy of this sheet is enclosed.

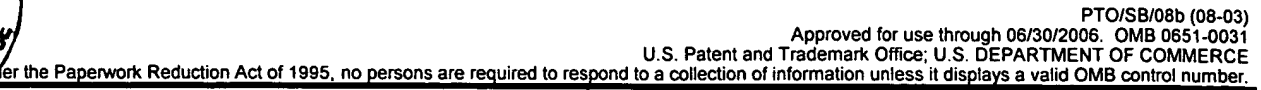
10/19/2005 FHETEK11 00000007 501065 10729095
01 FC:1806 180.00 DA

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Date

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Respectfully submitted,


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Attorney Docket Number	TSM03-0615
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